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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/522,541	03/10/2000	Maarten A. Koning	11283/9	5215

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EXAMINER

BANANKHAH, MAJID A

ART UNIT


PAPER NUMBER

2127

DATE MAILED: 05/22/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/522,541	Applicant(s) Koning	
	Examiner Majid Banankhah	Art Unit 2127	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on Mar 10, 2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
*See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s). 2,3,5,6 6) ☐ Other:

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1. This office action in response to application filed on March 10, 2000. Claims 1-21 are presented for examination.

2. Applicants are requested to note PTO-948 concerning notice of draftsman's patent drawing review. However, correction of the noted defect can be deferred until the application is allowed by the examiner. Applicants are reminded of the provision of MEP. 608.02(q) and (r) regarding a separate letter to the chief Draftsman.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-21 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Mateosian (Operating System Support- The Z8000 Way, Computer Design, May 1982).

As per claims 1, 14, 17-18, Mateosian et al. ("699") teach:

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- memory Space (memory, Fig. 1), a task (Fig. 3), a first task control block associated with the task and located in a first area of the memory space (Fig. 3, process manager), the first task control block including a number of first task information data structures that contain first task information (Fig.3, schedule, lock/unlock, suspend/resume, and create/destroy);

a second task control block associated with the task and located in a second area of the memory space (Fig. 3, event/queue manager, memory management, and Interrupt/Trap Handler), the second task control block including a number of second task information data structures that contain second task information (create/destroy, queue/dequeue, and wait/test/signal);

wherein the first area of the memory space is not directly accessible by the task, and the second area of the memory space is directly accessible by the task (The program status area pointer control registers and the system mode stack register are **all inaccessible from normal mode** [See: process manager and system status, such as suspend/resume, and lock/unlock], and normal mode stack register is accessible from system mode, page 256, Right col. Lines 1-42).

Per claims 2-3, current task data structure (page 258, left

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column, lines 35, Right Column, lines -21), context switch to execute the task (page 258, Right column, lines 33-59).

Per claim 4, first task control block includes a pointer data structure that contain a pointer to a location of the second task control block (stack register, Page 256, right col. Lines 35-42).

Per claims 5, and 15, the first area of the memory space is a system space (Fig. 1, ROM subsystem), and the second area of the memory is user state (Fig. 1, RAM subsystem).

Per claim 6, wherein the number of second task information data structures includes error status information for the task (Fig. 3, Event Queue, and wait/test/signal).

Per claim 7-8, wherein the number of second task information data structures includes a set of pointers to a set of standard modules for the task, and number of second task information data structures includes a pointer to environment variables for the task (call to the operating system elements from the applications software, and encoding system functions, Zero to 256, encoding one of the system functions page, 256, Right Column, lines 52-

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65).

Per claim 9-10, wherein the number of second task information data structures includes a pointer to context information for remote procedure calls made by the task (Fig. 3, Interrupt/Trap Handler, and context switch, and dispatch).

Per claim 11, and 19, wherein the number of second task information data structures includes a pointer to exception information (page 258, Right col. ,lines 47-64, interrupt/trap, and Event Queue).

Per claim 12, The system of claim 1, wherein the number of second task information data structures includes a user-definable spare field (Fig. 3, Even Manager, semaphore manager, and Create/Destroy).

Per claim 16, wherein the first task control block and the second task control block are located in a system space, the system space within the memory space (Fig. 1, ROM subsystem).

Per claim 13, a real-time operating system (Fig.1 Arcade game).

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Per claim 20-21, wherein the first number of state information values are saved on an interrupt stack (Page 258, Right col. Lines 47-64).

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Majid A. Banankhah** whose voice telephone number is (703) 308-6903. A voice mail service is also available at this number.

All response sent to U.S. Mail should be mailed to:
Commissioner of Patent and Trademarks
Washington, D.C. 20231

Hand-delivered responses should be brought to Crystal Park Two, 2021 Crystal Drive, Arlington. VA, Six Floor (Receptionist). All hand-delivered responses will be handled and entered by the docketing personnel. Please do not hand deliver responses to the Examiner.

All Formal or Official Faxes must be signed and sent to either (703) 308-9051 or (703) 308-9052. Official faxes will be handled and entered by the docketing personnel. The date of entry

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will correspond to the actual FAX reception date unless that date is a Saturday, Sunday, or a Federal Holiday within the District of Columbia, in which case the official date of receipt will be the next business day. The application file will be promptly forwarded to the Examiner unless the application file must be sent to another area of the office, e.g., Finance Division for fee charging, etc.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-9600.

Majid Banankhah

May 21, 2003

MAJID BANANKHAH
PRIMARY EXAMINER
